

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/666,353	Conf. No.:	9338
Applicant:	Visweswariah	TC/AU:	2825
Filed:	9/19/2003	Examiner:	Phallaka
Customer No.:	54275	Docket:	YOR920030403US1 (IBMY-0060)

Title: SYSTEM AND METHOD FOR STATISTICAL TIMING
ANALYSIS OF DIGITAL CIRCUITS

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

INTRODUCTORY COMMENTS:

In response to the Office Action of December 11, 2007, a two month extension of time having been paid concurrently herewith, please amend the above-referenced patent application as follows: